

PAGODA TRX ARCHITECTURE OVERVIEW

JUNE 2021

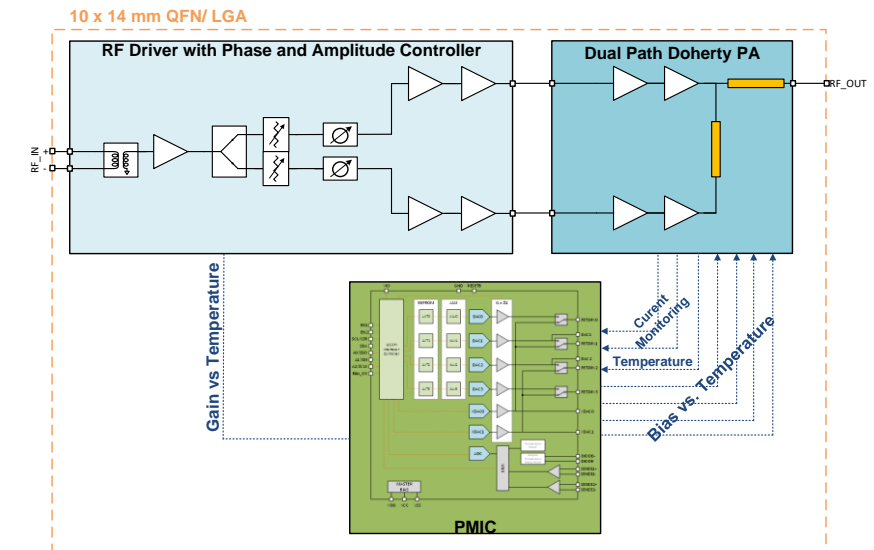
BIG IDEAS
FOR EVERY SPACE

PAGODA – NEW AAS TRX ARCHITECTURE USING RENESAS' PATENTED IP

(PHASE, AAMPLITUDE AND GATE-BIAS OPTIMIZER FOR DOHERTY AAMPLIFIERS)

Features

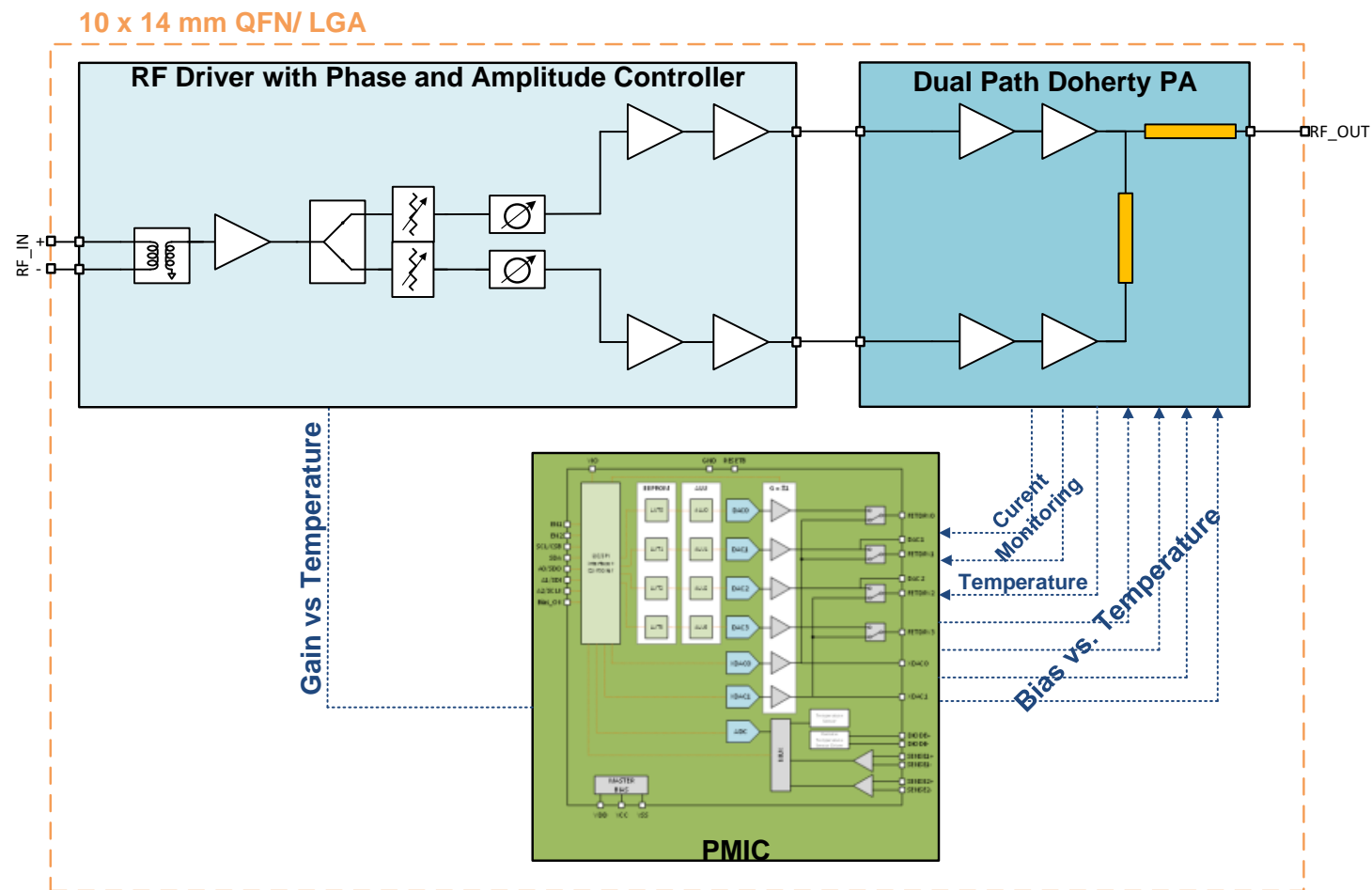
- In-Factory calibration of Gain & Doherty PA bias vs temperature
- PA's current monitoring for Idq Drift compensation (GaN)
- Temperature monitoring for bias and Gain compensation
- On the fly Phase & Power Split ratio adjustment for optimum TX performance (factory variations, efficiency and DPD) vs IBW & PAR & power mode (nominal Vsupply or low power mode with reduced Vsupply)
- LGA 10 x 14 mm or equivalent
- Pout 5-10W avg, Gain >40 dB



- Highly integrated, cost effective
- To be available as individually packaged solution or integrated into an MCM

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Thank you